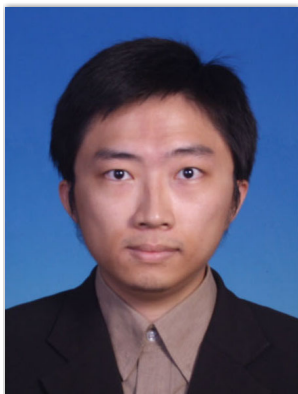


CHAI Chok Leong

Electrical and Electronic Engineer specializing in Embedded Systems



Temporary address

3B-31-03, BJ Court Condo,
Jalan Rumbia,
11900 Bayan Baru,
Penang, Malaysia

Permanent address

No. 374, Batu 6, Jalan Miri-Bintulu,
P.O.Box 2550, 98000 Miri,
Sarawak, Malaysia

Telephone

(+6) 017 803 1192

Email

chai@chokleong.com

Very detailed online résumé

chokleong.com/cv

Or scan this



Profile

Fresh graduate (Master's degree in 2011) in Electrical and Electronic Engineering with demonstrated ability to adapt to different working environments. Self-motivated and hard-working individual who takes his work seriously and is eager to learn and succeed.

Experience

Graduate trainee, Intel, Penang — April 2012 - Present

- **Intel**, 11900 *Bayan Lepas*, Malaysia - an American multinational semiconductor chip maker corporation.
- Designing and developing new test solution and software for the test conversion, validation, test library and test suite.

Intern software engineer, CEA, France — March - August 2011

- Final year work placement (six months) at **CEA**, 92265 *Fontenay-aux-Roses*, France - a French government-funded technological research organization.
- Developed with C / C++ on an existing large-scale industrial project for computer-assisted teleoperation for robots to increase clients' (control engineers) productivity.

Intern software engineer, INRIA, France — May - August 2010

- Fourth year work placement (four months) at **INRIA**, 78153 *Le Chesnay*, France - a French national research institution working on CyCab (electric vehicle) applications in C.
- With strong analytical skills, successfully tested, debugged and validated the synchronous multi-period feature of SynDEx (system level CAD software developed by INRIA) under Linux / RTAI (open source real-time OS) on CyCab and integrated the existing image-processing application into SynDEx.

Intern software engineer, Neutronic, France — July and October 2008

- Technical work placement (two months) at **Neutronic**, 77500 *Chelles*, France - a small fire alarm company doing programming in C for micro-controller PIC (Programmable Integrated Circuit) using Hi-Tech C (MPLab).
- Contributed to fire alarm projects by adding new features and upgrading certain applications onto newer chip.

Education

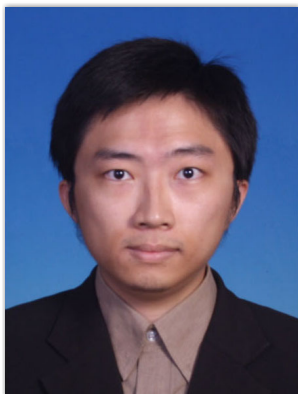
Master's degree in EE Engineering — Sept. 2006 - Sept. 2011

- Obtained a master's degree (*Diplôme d'ingénieur*) - a five-year program leading to the equivalent of an MSc Electronic Engineering from **ESIEE Engineering**, 93162 *Noisy-le-Grand*, France - a university of electrical and electronic engineering.
- Specialized in Embedded Systems. Average grade point 15.47 / 20 (French system) equivalent to CGPA¹ of 4.0. Courses included computer science, electronics and control theory.

¹ CGPA obtained by referring to the source below (regular courses taught in French):
<http://www.foster.washington.edu/centers/gbc/Documents/ug/exchange/FrenchGrade.pdf>

CHAI Chok Leong

Electrical and Electronic Engineer specializing in Embedded Systems



Temporary address

3B-31-03, BJ Court Condo,
Jalan Rumbia,
11900 Bayan Baru,
Penang, Malaysia

Permanent address

No. 374, Batu 6, Jalan Miri-Bintulu,
P.O.Box 2550, 98000 Miri,
Sarawak, Malaysia

Telephone

(+6) 017 803 1192

Email

chai@chokleong.com

Very detailed online résumé

chokleong.com/cv

Or scan this



Bachelor's degree in Mathematics and Computer Science – Sept. 2009

- Obtained a bachelor's degree (*Licence*) from **Université Marne-la-Vallée**, 77454 *Marne-la-Vallée*, France. Degree obtained by submitting the first three years' results from **ESIEE Engineering**. Average grade point 14 / 20 (French system).

A-Levels in Pure Maths and Physics – Sept. 2004 - Sept. 2006

- Obtained levels (*Baccalauréat*) equivalent to English A-Levels in pure maths and physics from **IUT**, 68000 *Colmar*, France. IUT is translated as University Institute for Technology. Average grade point 15.5 / 20 (French system).

- Learnt French at **CEL**, 68000 *Colmar*, France - a foreign language centre.

Competence

- Specialized in low-level programming for CAN, FPGA, DSP, micro-controllers and networking (TCP / IP).

- Experienced with Object-Oriented high-level programming.

- Regular user of C / C++, C#, Java, VHDL, shell script.

- Proficient user of Matlab / Simulink, Visual Studio, Eclipse, LabView, Quartus, Xilinx ISE, Microsoft Office, Unix / Linux.

- Multilingual Chinese (Native language), Malay (Fluent), English (Fluent) and French (Fluent), Cantonese (Fluent).

Reference

Mr DA SILVA SIMOES Max, research engineer at CEA, France. (<http://www.linkedin.com/in/maxsimo>)

More available on request.